

Killing Bugs in a Black Box with Model-based Mutation Testing

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> MT CPS Workshop Vienna, 11 Apr 2016



Acknowledgements

Joint work with

J. Auer · H. Brandl · W. Herzner · E. Jöbstl · W. Krenn · R. Korosec · F. Lorber · D. Nickovic · A. Rosenmann · R. Schlick · B.V. Schmidt · M. Tappler · S. Tiran

Strong Collaboration: Since 2008 with AIT Since 2011 with AVL



Projects

Past:

- CREDO: FP6, MBT of distributed systems
- MOGENTES: FP7, MBT of embedded systems, mutation testing, qualitative reasoning for testing hybrid systems
- TRUFAL: national, scalability of test-case generators via symbolic analysis
- MBAT: FP7, integration of methods and tools, MBT + consistency checking

Ongoing:

- CRYSTAL: FP7, integration of tools, MBT + requirements engineering
- TRUCONF: national, MBT + non-functional requirements + systems of systems



Agenda

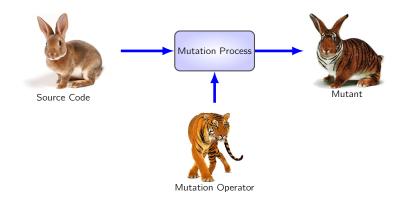
Model-based Mutation Testing

- Real-Time Systems
- Hybrid Systems
- Discrete Systems



Mutation Testing I

Step 1: Create mutants





Mutation Testing II

Step 2: Try to kill mutants



A test case kills a mutant if its run shows different behaviour.

Quality of tests:

How many mutants survived? [Lipton71, Hamlet77, DeMillo et al.78]

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Objective

Don't write test cases,

generate them!

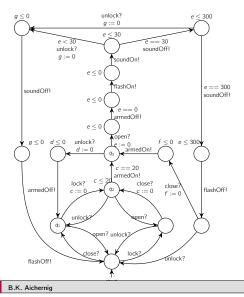
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Objective

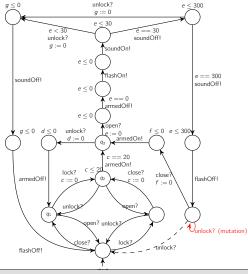
Don't write test cases, generate them!





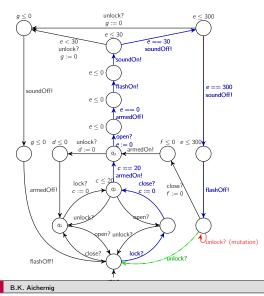
- Car alarm system model
- and a mutation representing a fault
- leading to non-conformance representing an observable failure
- resulting in a test case triggering this fault
- and propagating it to a visible failure





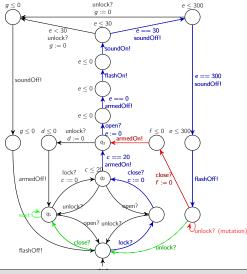
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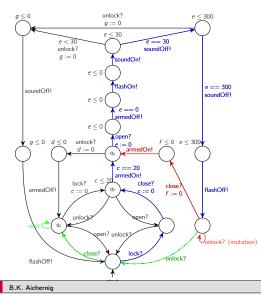




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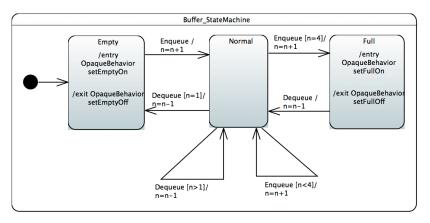
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What is a failure?



Fault-Propagation in Models

Abstract 5-place buffer model:

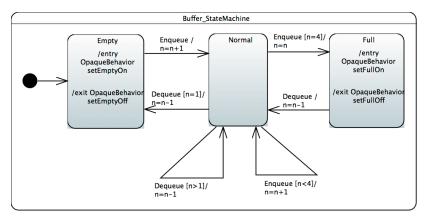


Counter variable n is internal!



Fault-Propagation in Models

Let's inject a fault:

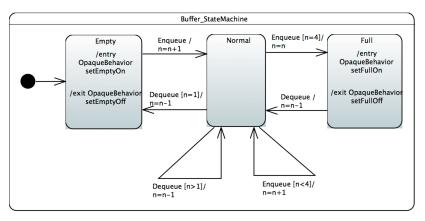


How does this fault propagate?



A Good Test Case

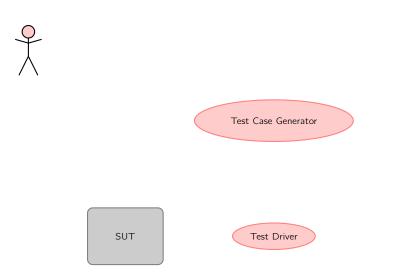
... triggers this fault and propagates it to a (visible) failure:



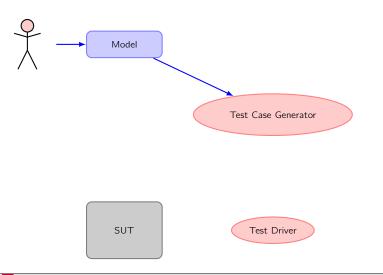
(!setEmptyOn, ?Enqueue, !setEmptyOff, ?Enqueue, ?Enqueue, ?Enqueue, ?Enqueue, !setFullOn, ?Dequeue, !setFullOff, ?Enqueue, !setFullOn)



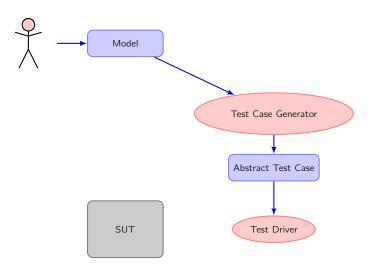




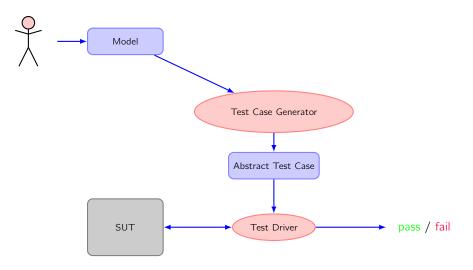




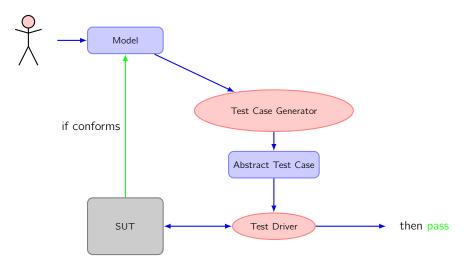




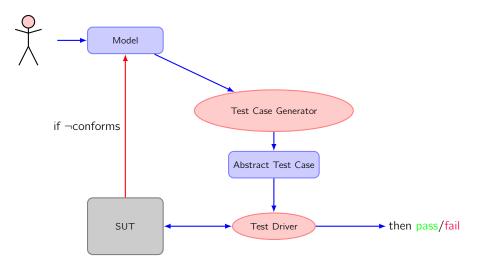




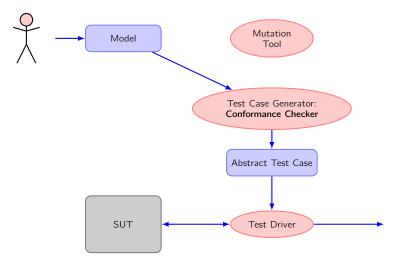




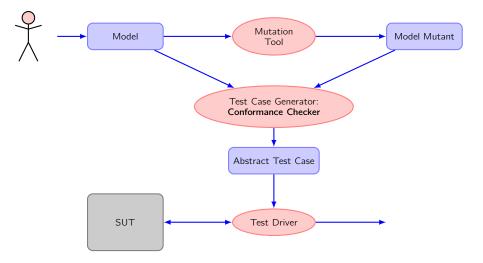




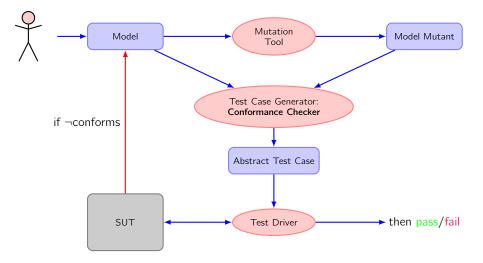




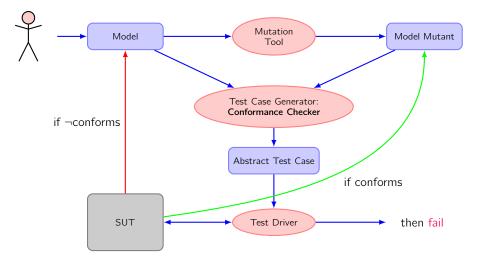




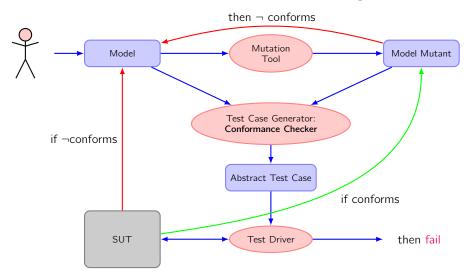














MoMuT Tools

${\sf MoMuT}$

- ► is a family of tools implementing Model-based Mutation Testing.
- ▶ is jointly developed and maintained by AIT and TU Graz
- supports different modelling styles:
 - MoMuT::UML (UML state machines)
 - MoMuT::OOAS (OO Action Systems)
 - MoMuT::QAS (Qualitative Action Systems)
 - MoMuT::TA (Timed Automata)
 - MoMuT::TAS (Timed Action Systems)
 - MoMuT::REQs (Synchronous Requirement Interfaces)

www.momut.org



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Conformance Relation of Timed Systems

... defines in a testing theory what constitutes a failure.

Definition (Timed input-output conformance – tioco [Krichen&Tripakis09])

Given a timed automaton Model and a Mutant with inputs and outputs

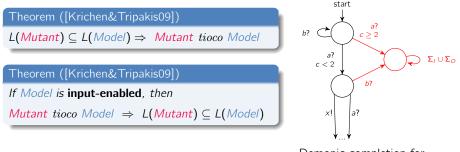
Mutant tioco Model iff

 $\forall \sigma \in L(Model) : out(Mutant after \sigma) \subseteq out(Model after \sigma)$

S	 set of all states	$A\mathrm{after}\sigma$	=	$\{s \in S \mid s_0 \xrightarrow{\sigma} s\}$
s_0	initial state	elapse(s)	=	$\{t > 0 \mid s \xrightarrow{t}\}$
σ	timed trace of labels	out(s)	=	$\{a \in \Sigma_O \mid s \xrightarrow{a}\} \cup \text{elapse}(s)$
Σ0	 output labels	out(S)	=	$\bigcup_{s \in S} \operatorname{out}(s)$



tioco and Language Inclusion



Demonic completion for deterministic TA

For deterministic TA,

reduce tioco check to language inclusion check (PSPACE-complete).



k-Bounded Language Inclusion

- ▶ Construct a formula φ_{A_I,A_S}^k that is satisfiable if $L(A_I) \not\subseteq L(A_S)$
 - providing a timed trace as witness

$$\begin{aligned} \varphi_{A_{I},A_{S}}^{k} &\equiv \\ & \bigwedge_{i=1}^{k} (d^{i} \geq 0 \land 1 \leq \alpha^{i} \leq |\Sigma|) \land i \geq 1 \land i \leq k \\ & 1 \leq i \leq k \\ & \text{init}_{A_{I}}(X_{I},C_{I}) \land \text{path}_{A_{I}}^{1,i-1}(\mathcal{A},D,X_{I},C_{I}) \\ & \text{init}_{A_{S}}(X_{S},C_{S}) \land \text{path}_{A_{S}}^{1,i-1}(\mathcal{A},D,X_{S},C_{S}) \\ & \text{path}_{A_{I}}^{i,i}(\mathcal{A},D,X_{I},C_{I}) \land \text{orpath}_{A_{S}}^{i,i}(\mathcal{A},D,X_{S},C_{S}) \end{aligned}$$
 (reach in model)

Variable sets: $x^{i} \in X$... location at step i $\alpha^{i} \in A$... i^{th} discrete action $d^{i} \in D$... i^{th} time delay $\{c^{i}, c^{*,i}\} \subseteq C$... clock valuation after i^{th} time and discrete step



Experimental Results I

- Bounded language inclusion check for deterministic Uppaal TA
- Implemented in Scala calling SMT solver Z3
- Car alarm system characteristics: deterministic,
 - 5 clock variables, 16 locations, 25 transitions.
- ▶ 8 mutation operators \rightarrow 1,320 mutants
- Overall runtime: 30 minutes (k = 12)

Runtime details



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Depth	Bounded Model Checking				Symbolic Execution			
	Mean	Median	Max	Min	Mean	Median	Max	Min
12	1.4 <i>s</i>	1.1 <i>s</i>	33 <i>s</i>	0.07 <i>s</i>				

Runtime details



Timed Action Systems

```
types {
     State = [ ... | Flash | FlashSound | Silent | SwitchOffAlarm | ... ]; }
   state {
     loc : State; }
 4
   clocks [Real]{ c;d;e;f;g }
 5
   init {
6
     loc := OpenAndUnlocked; }
7
   invariant {
8
     if loc == Flash
                       then e \leq 0:
9
   if loc == FlashSound then e <= 30;
10
    if loc == Silent
11
                       then e <= 300:
12
    .... }
   actions {
13
     !soundOn#1() if loc == Flash && e == 0 then { loc := FlashSound; };
14
15
     !soundOff#1() if loc == FlashSound && e == 30 then { loc := Silent ; };
16
17
     \operatorname{Punlock} () resets q if loc == FlashSound && e < 30 then { loc := SwitchOffAlarm; };
18
19
     .... }
```



Symbolic Execution of Timed Action Systems

path conditions ... blue symbolic (clock) states ... red



path conditions ... blue symbolic (clock) states ... red



path conditions ... blue symbolic (clock) states ... red



$$s_{0} pc = \overline{pc}$$

$$q_{c} = \{e \mapsto \overline{d}, \ldots\}$$

$$symbolic (clock) states \ldots red$$

$$symbolic (clock) states \ldots$$

$$g_{c} = \overline{pc}$$

$$q = \{loc \mapsto FlashSound, \ldots\}$$

$$delay(d)$$

$$s_{2} pc = \overline{pc} \land Flash = Flash \rightarrow \overline{d} + d \leq 30 \land \ldots$$

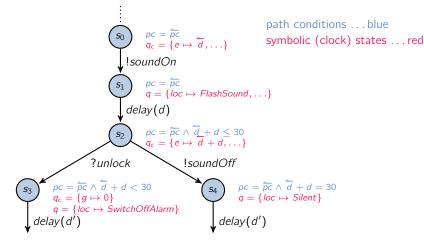
$$q_{c} = \{e \mapsto \overline{d} + d, \ldots\}$$



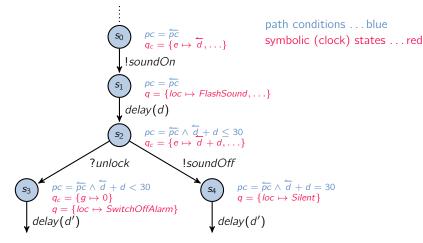
$$\begin{array}{c} \vdots \\ s_{0} \\ g_{c} = \{ e \mapsto \overleftarrow{d}, \ldots \} \\ \downarrow soundOn \\ \hline s_{1} \\ g_{c} = \{ loc \mapsto FlashSound, \ldots \} \\ \downarrow delay(d) \\ \hline s_{2} \\ g_{c} = \{ e \mapsto \overleftarrow{d} + d, \ldots \} \end{array}$$

path conditions ... blue symbolic (clock) states ... red









Provides all symbolic timed traces through model!

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Conformance Checking via Symbolic Execution

- Bounded implicit product graph exploration
- Simultaneous symbolic execution of all model traces
- ▶ Non-conformance checks (stioco) of the form:

 $pc_q \dots path$ condition of symbolic state q



Conformance Checking via Symbolic Execution

- Bounded implicit product graph exploration
- Simultaneous symbolic execution of all model traces
- ▶ Non-conformance checks (stioco) of the form:

 $\exists q_{fail} \in \underbrace{ModelStates}_{all symbolic states after current trace}$

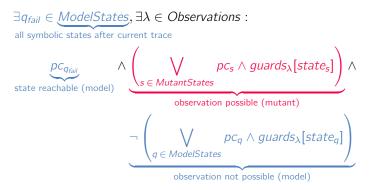
 $\underbrace{PC_{q_{fail}}}_{\text{state reachable (model)}}$

 $pc_q \dots path$ condition of symbolic state q



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Runtime details



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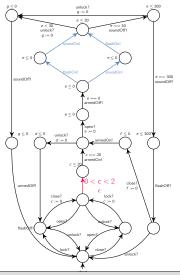
Depth	Βοι	inded Mod	el Chec	king		Symbolic I	Execution	
	Mean	Median	Max	Min	Mean	Median	Max	Min
12	1.4 <i>s</i>	1.1 <i>s</i>	33 <i>s</i>	0.07 <i>s</i>	1.7 <i>s</i>	0.02 <i>s</i>	38.83 <i>s</i>	$\sim 0s$

Runtime details



Experimental Results III

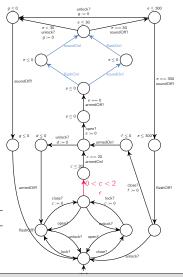
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- Car Alarm System: silent transition with non-deterministic delay
- Plus underspecification in switching on alarm
- 3 equivalent mutants timed out after 10min





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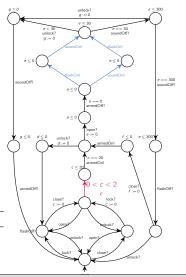


Depth				
	Mean	Median	Max	Min
12	0.79 <i>s</i>	0.06 <i>s</i>	360.84 <i>s</i>	$\sim 0s$



Experimental Results III

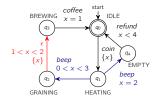
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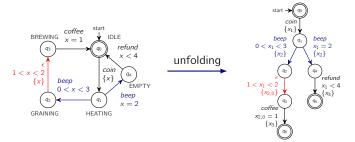
Depth				
	Mean	Median	Max	Min
12	0.79 <i>s</i>	0.06 <i>s</i>	360.84 <i>s</i>	$\sim 0s$

... and the bounded model checking?

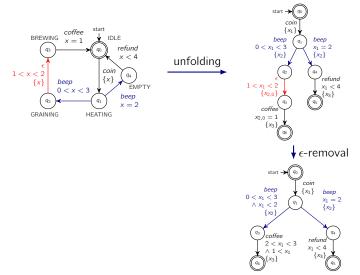




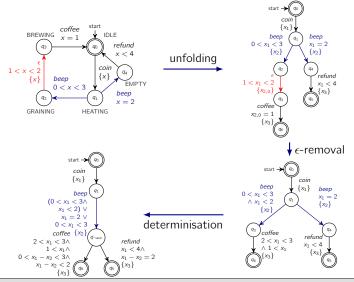






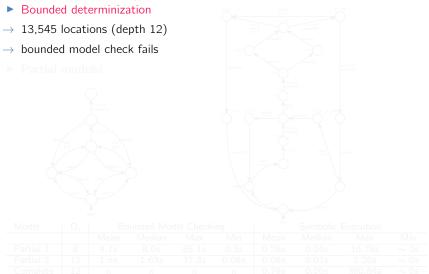








Experimental Results IV

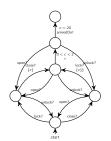


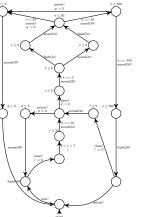
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Experimental Results IV

- Bounded determinization
- \rightarrow 13,545 locations (depth 12)
- ightarrow bounded model check fails
- Partial models!



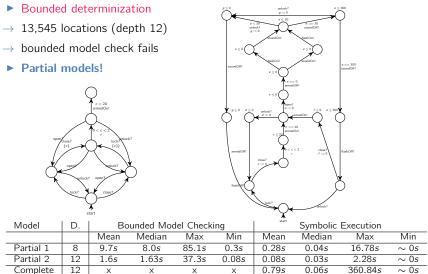


			start		

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Experimental Results IV



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Experimental Results V

Adding data variable and parameters to

- deterministic Car Alarm System with one clock
- 3-digit PIN code for unlocking
- No negative effects, even with higher digit PIN codes

Symbolic execution faster with 1 clock (0.24s) than with 5 clocks (1.7s)



Experimental Results V

Adding data variable and parameters to

- deterministic Car Alarm System with one clock
- 3-digit PIN code for unlocking
- ▶ No negative effects, even with higher digit PIN codes

► Symbolic execution faster with 1 clock (0.24*s*) than with 5 clocks (1.7*s*)

	Depth	B	ounded Mo	del Checki	ng	Symbolic Execution			
-		Mean	Median	Max	Min	Mean	Median	Max	Min
	8	1.46 <i>s</i>	0.28 <i>s</i>	59.41 <i>s</i>	0.12 <i>s</i>	0.07 <i>s</i>	0.05 <i>s</i>	0.82 <i>s</i>	$\sim 0s$
	12	4.12 <i>s</i>	0.35 <i>s</i>	35.41 <i>s</i>	0.13 <i>s</i>	0.24 <i>s</i>	0.05 <i>s</i>	3.67 <i>s</i>	$\sim 0s$



Real-Time Systems Summary

Symbolic execution (SE) seems to perform better, but no clear winner!

- Number of clocks:
 - BMC: small impact (was faster in deterministic case)
 - SE: high impact
- ▶ Non-determinism: is an obstacle for conformance checking
 - ▶ BMC: state-space explosion → partial models
 - ▶ SE: lowered performance (40s vs. 6min) \rightarrow 3 mutants timed out
- Statistical outliers: due to equivalent mutants
 - BMC: runtime almost equal
 - SE: extreme differences due to optimisations

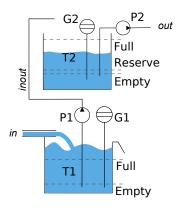


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A Hybrid System: Two Tank System



- P1, P2 ... water pumps
- G1, G2 ... water-level sensors

Requirements:

- P1 starts pumping, if T2 below Reserve and T1 is full
- until T1 is empty or T2 is full
- P2 is controlled by button WaterRequest
- runs if there is water in T2.
- ▶ Note: T1 may overflow



Related Work

Hybrid Systems

- Hybrid Automata (Alur, Courcoubetis, Henzinger, Ho 93)
- Action Systems [Back,Kurki-Suonio 83]
- Hybrid Action Systems [Rönkkö, Ravn, Sere 03]
- Qualitative Reasoning [Kuipers 94]
- ► Testing
 - Mutation Testing [Hamlet 77, De Millo et al. 78]
 - Input-Output Conformance [Brinksma, Tretmans 92]



Abstraction 1: Action Systems

Modeling the Controller

Controller. P1 running, P2 running : Bool, var out*. inout* : Real P1 running := false; $P2^{-}$ running := false; out := 0; inout := 0;do $g_1 \rightarrow P1_running := true; inout := (0, Max]$ $g_2 \rightarrow P1_running := false; inout := 0$ $g_3 \rightarrow P2_running := true; out := (0, Max]$ $g_4 \rightarrow P2$ _running := false; out := 0 od WaterRequest, x_1, x_2



Abstraction 1: Action Systems

Modeling the Controller

Cont	roller:		Guards	5:	
[var	P1_running, P2_running : Bool, out*, inout* : Real	•	$g_1 =_{df}$	$x_2 \leq Reserve \land x_1 = Full \land \neg P1_running$
		P1_running := false; P2_running := false; out := 0; inout := 0;		$g_2 =_{df}$	$\begin{array}{l} P1_running \land \\ (x_1 \leq Empty \lor x_2 = Full) \end{array}$
	do	$g_1 \rightarrow P1_running := true; inout := (0, Max]$ $g_2 \rightarrow P1_running := false; inout := 0$	•	$g_3 =_{df}$	WaterRequest ∧ ¬P2_running ∧ x ₂ > Reserve
	od	$ \begin{array}{l} \square \\ g_3 \rightarrow P2_running := true; out := (0, Max] \\ \square \\ g_4 \rightarrow P2_running := false; out := 0 \end{array} $	•	$g_4 =_{df}$	$\begin{array}{l} P2_running \land \\ (\neg WaterRequest \lor \\ x_2 = Empty) \end{array}$
]	:	$WaterRequest, x_1, x_2$			



Why Action Systems?

- Well-suited for embedded systems modeling
- Action view maps naturally to LTS testing theories
- Solid foundation:
 - precise semantics
 - refinement
- Compositional modeling
- Many extensions available:
 - object-orientation
 - hybrid systems



Hybrid Action Systems

Environment:

$$|[var x_1^*, x_2^* : Real$$

$$x_1 := 0; x_2 := 0$$
alt
$$g_1 \rightarrow \dots$$

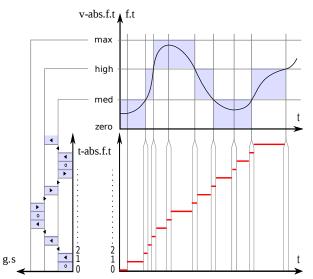
$$\vdots$$
with
$$\neg(g_1 \vee \dots) :\rightarrow \dot{x}_1 = (in - inout)/A_1 \wedge \dot{x}_2 = (inout - out)/A_2$$

$$|] : inout, out$$

Rönkkö, M., Ravn, A.P., Sere, K.: Hybrid action systems. Theoretical Computer Science 290 (2003) 937–973.

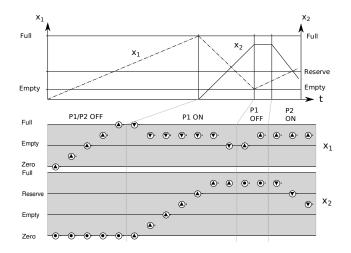


Abstraction 2: Qualitative Flows





Example Qualitative Flow of Water Tanks





Qualitative Reasoning (QR)

- QR originates from Artificial Intelligence
- Common sense reasoning about physical systems with possibly incomplete knowledge.
- ▶ Ordinary Differential Equations (ODE)
 → Qualitative Differential Equations (QDE):

 $\dot{x}_1 = (in - inout)/A_1 \rightarrow d/dt(x_1, diff_1) \wedge add(diff_1, inout, in)$

Arithmetic is reduced to sign algebra:

$$5 - 1 = 4 \quad \rightarrow \quad [+] + [-] = [+] \mid [-] \\ -3 * 2 = -6 \quad \rightarrow \quad [-] * [+] = [-]$$

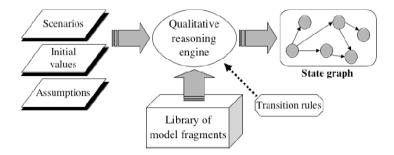


Qualitative Action Systems

$$\begin{array}{ccccc} |[& \mathsf{var} & x_1^*, x_2^* : \mathit{Real} \\ \bullet & & \\ & x_1 := 0; x_2 := 0 \\ \mathsf{alt} & & \\ &$$



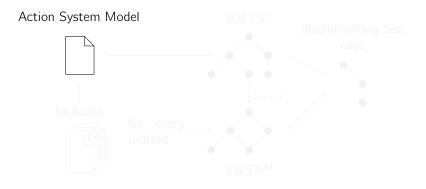
Qualitative Simulation



Implementations:

- QSIM (Lisp)
- Garp3 (SWI-Prolog)
- ASIM (GNU-Prolog)

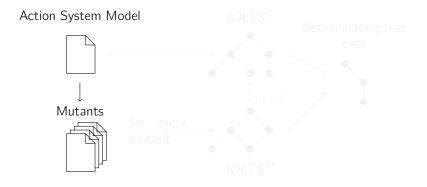




ioco ... input-output conformance

B.K. Aichernig

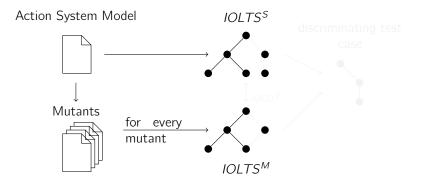




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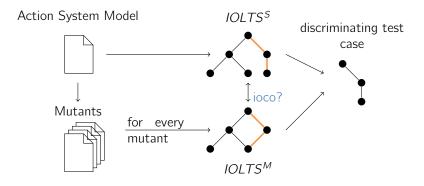




ioco ... input-output conformance

B.K. Aichernig





ioco ... input-output conformance

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Conformance Checking

- Event-view: labeled actions
- Input and Output Labels

Def. IOCO [Tretmans 96]

 $\forall \sigma \in \text{Straces}(Model) : \text{out}(Mutant after \sigma) \subseteq \text{out}(Model after \sigma)$

out ... outputs labels + quiescence after ... reachable states after trace

ioco supports: partial, non-deterministic models

ioco-checker Ulysses

- implemented in GNU Prolog
- explores discrete actions + qualitative flows
- builds synchronous product modulo ioco
- ▶ highly non-deterministic → on-the-fly determinization



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Generating a Testcase: Original Model

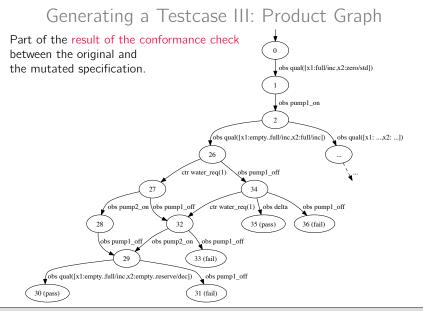
System =		
[var		$x_1 : T1, x_2 : T2, out, inout : FR,$
		$diff_1, diff_2 : NZP,$
		p1_running, p2_running, wr : Bool
	•	$x_1 := (0, 0); x_2 := (0, 0);$
		out := (0, 0); inout := (0, 0); wr := false
		p1_running := false; p2_running := false
alt		obs pump1_on : $g_1 \rightarrow p1$ _running := true;
	_	inout := (0Max, 0)
		obs pump1_off: $g_2 \rightarrow p1_running := false;$
	_	inout := $(0,0)$
		obs pump2_on : $g_3 \rightarrow p2$ _running := true;
	_	out := (0Max, 0)
		obs pump2 off : $g_4 \rightarrow p2$ running := false;
	_	out := (0, 0)
		$ctr water req(X) : g_5 \rightarrow wr := X$
with		$\neg(g_1 \lor g_2 \lor g_3 \lor g_4 \lor g_5) : \rightarrow$
		$add(diff_2, out, inout) \land add(diff_1, inout, in) \land$
		$d/dt(x_1, diff_1) \wedge d/dt(x_2, diff_2)$
] : in		



Generating a Testcase II: Mutated Model

System =		
[var		$x_1 : T1, x_2 : T2, out, inout : FR,$
		$diff_1, diff_2 : NZP,$
		p1_running, p2_running, wr : Bool
	•	$x_1 := (0, 0); x_2 := (0, 0);$
		out := (0, 0); inout := (0, 0); wr := false
		p1_running := false; p2_running := false
alt		obs pump1_on : $g_1 \rightarrow p1$ _running := true;
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		obs pump1_off : $g_2 \rightarrow p1$ _running := true;
		inout := (0, 0)
		obs pump2_on : $g_3 \rightarrow p2$ _running := true;
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		$add(diff_2, out, inout) \land add(diff_1, inout, in) \land$
		$d/dt(x_1, diff_1) \wedge d/dt(x_2, diff_2)$
] : <i>in</i>		







Results

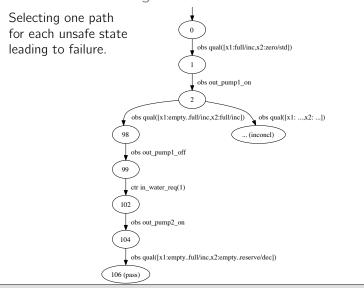
Mut.	No.	Avg.Time	Averag	ge No.	_/		=
Op.	Mutants	[s]	States	Trans.	¥	No.	Perc.
ASO	10	13.9	64	117	7	3	30%
ENO	6	7.6	68	120	5	1	17%
ERO	20	12.9	62	110	20	0	0%
LRO	13	12.8	93	168	9	4	31%
MCO	16	12.8	70	126	10	6	38%
RRO	12	12.0	40	73	10	2	17%
Total	77	12.0	66	119	61	16	21%

- ASO ... Association Shift Operator
- ENO ... Expression Negation Operator
- ERO ... Event Replacement Operator

- LRO ... Logical Operator Replacement
- MCO ... Missing Condition Operator
- RRO ... Relational Replacement Operator



Generating a Testcase IV: Linear TC

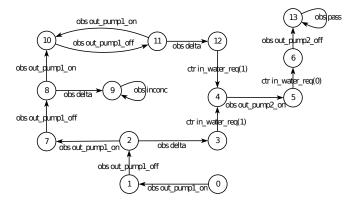


B.K. Aichernig



Generating a Testcase V: Adaptive TC

A test graph including all paths to a given unsafe state leading to failure.



Qualitative events are internal (not visible).



Hybrid Systems Summary

- AI meets FM: qualitative reasoning
- Requirements \rightarrow incomplete qualitative models
- Model exploration: controller (discrete) + environment (qualitative)
- TCG based on mutation testing and ioco conformance checking
- Different strategies for selecting test case



Agenda

- Model-based Mutation Testing
- Real-Time Systems
- Hybrid Systems
- Discrete Systems



Discrete Systems: MoMuT::UML

Applications:

- Car Alarm System (Ford)
- Railway Interlocking System (Thales)
- Automotive Meassurement Device: Particle Counter (AVL)



SUT: AVL489 Particle Counter

- One of AVL's automotive measurement devices
- Measures particle number concentrations in exhaust gas
- Focus: testing of the control logic
- AVL uses virtual test-beds with simulated devices for integration and regression testing.
- We tested a simulation of the particle counter:
 - Matlab Simulink model compiled to real-time executable
 - Same interface as real device!





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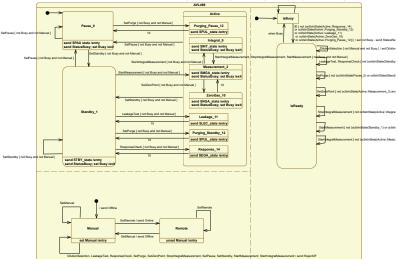
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UML Test Model of AVL489

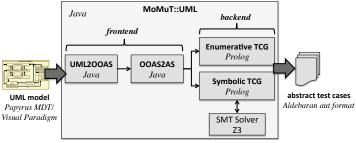


LeakageTest, ResponseCheck, SetPurge, SetZeroPoint, StopIntegral/Neasurement, SetStandby, StartMeasurement, StartIntegral/Neasurement, SetPlause, DilutionSu



MoMuT::UML

- Test-case generator of AIT and TU Graz
- Implementing model-based mutation testing for UML state machines



Architecture of the MoMuT::UML tool chain

AS ... Action Systems [Back83]

OOAS ... Object-Oriented Action Systems



Abstract Test Case of AVL489

concrete C#



Test Execution on Particle Counter

We found several bugs in the SUT:

- ► Forbidden changes of operating state while busy
 - ▶ Pause \rightarrow Standby
 - ▶ Normal Measurement \rightarrow Integral Measurement
- Ignoring high-frequent input without error-messages
- Loss of error messages in client for remote control of the device



Refinement + ioco Conformance Checking

Refinement:

- state-based
- predicative semantics

Def. Refinement [Hoare & He 98]

 $\forall s, s' : Mutant(s, s') \Rightarrow Model(s, s')$

s ... state before s' ... state after execution

Input-Output Conformance:

- event-based
- io labelled transition systems

Def. IOCO [Tretmans 96]

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out ... outputs labels + quiescence after ... reachable states after trace

New combined conformance checking:

- Refinement checker searches for faulty state (fast)
- loco checker looks if faulty state propagates to different observations



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Symbolic Refinement Checking

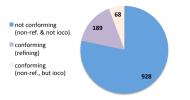
Is non-refinement reachable?

 $\exists s, s', tr, tr' : reachable(s, tr) \land Mutant(s, s', tr, tr') \land \neg Model(s, s', tr, tr')$

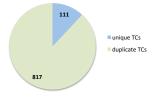
s ... state befores' ... states after executiontr ... trace of labels beforetr' ... trace of labels after execution



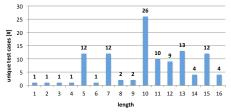
TCG Particle Counter



(a) Breakup into conforming and not conforming model mutants.



(b) Breakup into unique and duplicate test cases.



(c) Lengths of the unique test cases.



Fault Propagation

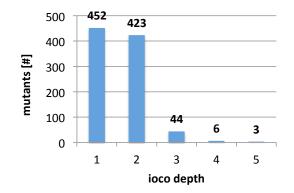


Figure: Number of steps from fault to failure (ioco depths)



Run-times

... for combined conformance checking (in min., max. depth 15+5) :

		conforming	conforming	not conforming	total
		(refining)	(non-ref., but ioco)	(non-ref. & not ioco)	totai
mutants [#]		189	68	928	1185
	Σ	6.1 h	7.7	7.1 h	13.3 h
ref. check	ϕ	1.9	6.8 sec	27 sec	40 sec
rel. check	max	4.3	1.8	3.9	4.3
	Σ	-	0.7 h	1.7 h	2.4 h
ioco check	ϕ	-	38 sec	7 sec	7.4 sec
IOCO CHECK	max	-	2	27 sec	2
	Σ	-	-	22.9	22.9
tc constr.	ϕ	-	-	1.5 sec	1.2 sec
	max	-	-	3.7 sec	3.7 sec
	Σ	6.1 h	0.9 h	9.2 h	16.2 h
total	ϕ	1.9	0.8	0.6	0.8
without logging	max	4.3	2.2	4.1	4.3



Run-times

... comparison to stand-alone ioco check (in min., max. depth 10):

		not ioco	ioco	total
mutants [#]		719	466	1185
	Σ	9.8 h	22.8 h	32.6 h
time – joco check	ϕ	0.8	2.9	1.7
LITTE - IOCO CHECK	max	3.9	5.2	5.2
	Σ	19	-	19
time – tc constr.	ϕ	1.6 sec	-	1 sec
	max	5.8 sec	-	5.8 sec
	Σ	10.1 h	22.8 h	32.9 h
total without logging	ϕ	0.8	2.9	1.7
total without logging	max	3.9	5.2	5.2

appr. 16h vs. 33h



Discrete Systems Summary

- Fault propagation important for test-case design
- Faster test-case generator
 - find fault fast (refinement check)
 - analyze if fault propagates to failure (ioco check)
- Optimized refinement check
 - incremental SMT solving, state caching
 - exploiting the location of mutation
 - checking if existing test cases cover next fault
- Applied at AVL: many bugs found [TAP 2014]



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Synchronous Systems – MoMuT::REQs

Contract-based Requirement Interfaces:

- Synchronous assume-guarantee pairs
- Combined via conjunction
- Efficient SMT solving

Application: Airbag Chip (Infineon)

Inputs coin, teabutton, coffeebutton; Outputs coffee, tea; Internals paid;

 $\{I\}$ not paid and not coffee and not tea $\{R1\}$ assume coin'

guarantee paid'

- {R2} assume paid and teabutton' and not coffeebutton' guarantee tea' and not paid'
- {R3} assume paid and coffeebutton ' and not teabutton' guarantee coffee ' and not paid '
- {R4} assume teabutton' and coffeebutton'
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Bernhard K. Aichernig, Klaus Hörmaier, Florian Lorber, Dejan Nickovic, Stefan Tiran. *Require*, *Test and Trace IT*, FMICS 2015

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- Real-Time Systems: Timed Automata
- Hybrid Systems: Action Systems + Qualitative Reasoning
- Discrete Systems: UML
- Synchronous Systems: Assume-Guarantee Contracts
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References

Real-Time Systems

- B.K. Aichernig, F. Lorber, M. Tappler: Conformance Checking of Real-Time Models -Symbolic Execution vs. Bounded Model Checking. Theory and Practice of Formal Methods 2016: 15-32
- F. Lorber, A. Rosenmann, D. Nickovic, B.K. Aichernig: Bounded Determinization of Timed Automata with Silent Transitions. FORMATS 2015: 288-304
- B.K. Aichernig, F. Lorber, D. Nickovic: Time for Mutants Model-Based Mutation Testing with Timed Automata. TAP 2013: 20-38

Hybrid Systems

- B.K. Aichernig, H. Brandl, E. Jöbstl, W. Krenn: Model-Based Mutation Testing of Hybrid Systems. FMCO 2009: 228-249
- B. K. Aichernig, H. Brandl, W. Krenn: Qualitative Action Systems. ICFEM 2009: 206-225

Discrete Systems

- B.K. Aichernig, J. Auer, E. Jöbstl, R. Korosec, W. Krenn, R. Schlick, B.V. Schmidt: Model-Based Mutation Testing of an Industrial Measurement Device. TAP 2014: 1-19
- Willibald Krenn, Rupert Schlick, Stefan Tiran, Bernhard K. Aichernig, Elisabeth Jöbstl, Harald Brandl: MoMut: UML Model-Based Mutation Testing for UML. ICST 2015: 1-8